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## **PATENT**

JN 2 7 2006 g

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICATION OF:

SIDNEY B. RIGG ET AL.

EXAMINER:

HOWARD WEISS

APPLICATION NO.:

10/713,878

ART UNIT:

2814

FILED:

NOVEMBER 13, 2003

CONF. NO:

1111

For:

METHODS FOR FORMING VIAS IN MICROELECTRONIC DEVICES, AND

METHODS FOR PACKAGING MICROELECTRONIC DEVICES

## **Comments on Statement of Reasons for Allowance**

Mail Stop Issue Fee Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

In the Notice of Allowability mailed March 28, 2006, the Examiner allowed claims 1-15, 17-20, 22 and 25-30. Although the applicants' attorney agrees with the Examiner's conclusion that these claims are allowable, the applicants' attorney notes that the claims may be allowable for reasons other than those identified by the Examiner and does not concede that the Examiner's characterizations of the terms of the claims and the prior art are correct.

Date: 1/1/2 77 7006

Respectfully submitted,

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